



Patent #3/CFR
8/23/2
Sunder

Attorney's Docket No. 018760-016

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Shinichi TAKAGI et al

Application No.: 09/987,153

Filed: November 13, 2001

For: SEMICONDUCTOR ELEMENT
MODULE AND SEMICONDUCTOR
DEVICE WHICH PREVENTS SHORT
CIRCUITING

Group Art Unit: 2811

Examiner: Unassigned

REQUEST FOR SECOND CORRECTED OFFICIAL FILING RECEIPT

Assistant Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
Washington, D.C. 20231

Sir:

Enclosed is a copy of a corrected Official Filing Receipt marked in red to show correction that is needed. The correction is as follows.

Correct the Title to read: Semiconductor Element Module and **Semiconductor** Device which Prevents Short Circuiting

Issuance of a second corrected Official Filing Receipt is respectfully requested.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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Date: April 30, 2002

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/987,153	11/13/2001	2811	3428	019519-312	6	62	29

21839
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CONFIRMATION NO. 6439
CORRECTED FILING RECEIPT



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Date Mailed: 01/25/2002

Receipt is acknowledged of this non-provisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

Mitsubishi Denki Kabushiki Kaisha;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A REI OF 09/138,562 08/24/1998 PAT 5,986,337

Foreign Applications

JAPAN 9-315260 11/17/1997
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Projected Publication Date: Not Applicable

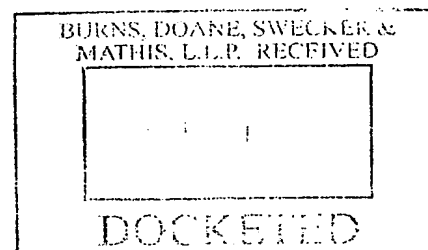
N n-Publication Request: No

Early Publication Request: No

Title

Semiconductor element module and semiconductor device which prevents short circuiting

~~semiconductor~~
semiconductor



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